

SELECTABLE MEMORY WORD LINE DEACTIVATION

Abstract of the Disclosure

[0034] Circuitry and methods allow selected memory word lines (WLs) to be deactivated without using a
5 global deactivate signal. All active WLs do not therefore have to be deactivated simultaneously, which can cause voltage at a common deactivate node to rise undesirably. This undesirable voltage rise can adversely affect a system by, for example,
10 inadvertently activating an inactive WL. The invention advantageously limits the voltage fluctuation at the common deactivate node.